

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/780,031 Confirmation No.: 9275
First Named Inventor: Park, Jongmin Filing Date: 17 February 2004
Group Art Unit: 2824 Examiner: Wendler, Eric J.
Atty. Docket No.: R-0003 US
Title: Technique for Programming Floating-gate Transistor Used in
Circuitry Such as Flash EPROM
Assignee(s): ProMOS Technologies Inc.

Mountain View, California
22 November 2005

**MAIL STOP RCE
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450**

**REQUEST FOR CONTINUED EXAMINATION
UNDER 37 CFR 1.114**

Sir:

This is a Request for Continued Examination ("RCE") of the above patent application under 37 CFR 1.114.

Please enter or/and consider each following document which is enclosed or was previously submitted:

1. Preliminary Amendment dated 22 November 2005;
2. Information Disclosure Statement ("IDS") dated 22 November 2005;
3. Substitute PTO Form 1449 accompanying 22 November 2005 IDS; and
4. Copy of one cited reference accompanying 22 November 2005 IDS.

The RCE fee required under 37 CFR 1.17(e) is authorized in an accompanying transmittal letter.

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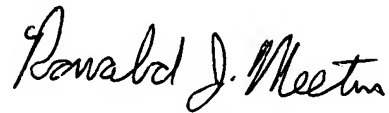
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Please telephone Attorney for Applicant(s) at 650-964-9767 if there are any questions.

EXPRESS MAIL LABEL NO.:

EV 500 311 315 US

Respectfully submitted,



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